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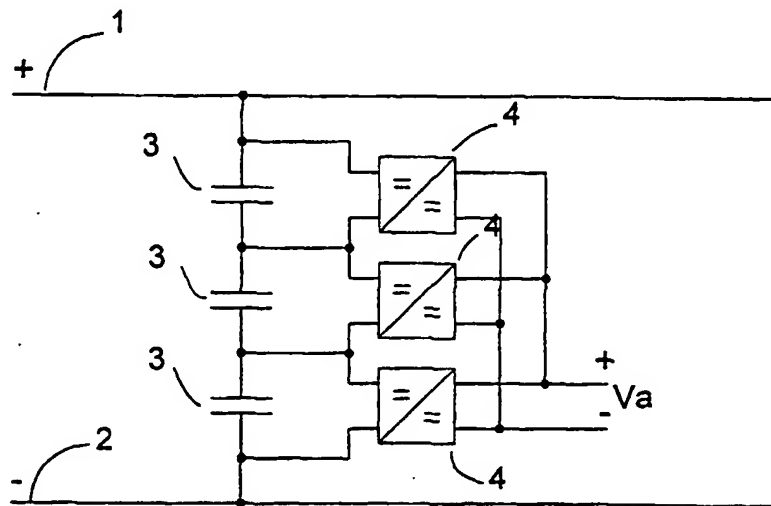
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(54) Title: VOLTAGE BALANCING IN INTERMEDIATE CIRCUIT CAPACITORS



(57) Abstract: A balancing circuit for voltages of a series connection of capacitors, particularly for intermediate circuit capacitors (3) of an inverter, there being at least two intermediate circuit capacitors connected in series over intermediate circuit voltage. The balancing circuit comprises capacitor-specific freely oscillating inverters (4), the input poles of which are connected in parallel with the capacitor corresponding to the inverter and the output poles of which are connected in parallel to provide a voltage source (Va).

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VOLTAGE BALANCING IN INTERMEDIATE CIRCUIT CAPACITORS

BACKGROUND OF THE INVENTION

The invention relates to a balancing circuit for voltages of a series connection of capacitors, particularly for intermediate circuit capacitors of a frequency converter, there being at least two intermediate circuit capacitors connected in series over intermediate circuit voltage.

Series-connected electrolyte capacitors are usually used as the energy reserve of the DC side in frequency converters. The number of capacitors to be connected in series depends on the supply voltage of the frequency converter, being usually one capacitor for 230 volts, two for 400 to 500 volts, three for 690 volts and four for 1000 volts. Series connections of capacitors can also be connected in parallel in an intermediate circuit. The number of parallel connections depends on the output current of the frequency converter.

The leakage currents of capacitors typically differ from one another, which means that the supply voltage of the static state acting over the series connection is not divided evenly between the capacitors. This may result in a situation where a single capacitor is subjected to a voltage which exceeds the allowed limit in the dynamic state due to the influence of current ripple and capacitance tolerances, for example. For this reason, 'balancing resistors' are usually connected in parallel with the capacitors, the current flowing through the resistors being much higher than the leakage current of the capacitors. In that case the voltage distribution in the static state is mainly determined by the resistance ratios of the resistors. It is also known in the art to use active components in addition to the resistors, e.g. emitter follower connections, which provide stricter restriction without an unreasonable increase in the power loss. However, the use of active components increases the component costs.

Typical balancing resistance for one capacitor in a frequency converter of 100 kVA is 22 kilo ohms, its power dissipation being 5.2 watts with 500 volts, for example. Since with this voltage there are two capacitors and resistors in series, the total power dissipation is 10.4 watts. With higher voltages the power dissipation is naturally even greater.

To operate the frequency converter needs a certain amount of auxiliary power for control circuits and gate drivers. This power is typically 10 to 20 watts in the case of a frequency converter of 100 kVA. It is easy to note that the amount of waste heat produced in the balancing resistors is nearly equal

to the amount of auxiliary power needed by the whole frequency converter. Thus it would be highly advantageous if the power dissipation required by balancing of capacitors could be utilized as the auxiliary power of the device.

BRIEF DESCRIPTION OF THE INVENTION

5 An object of the invention is to provide a circuit which allows to avoid the above-mentioned drawbacks and to balance voltages of a series connection of capacitors in a reliable manner so that an auxiliary voltage source is formed during voltage balancing. This object is achieved with a circuit according to the invention which is characterized in that the balancing circuit comprises capacitor-specific freely oscillating inverters, the input poles of
10 which are connected in parallel with the capacitor corresponding to the inverter and the output poles of which are connected in parallel to provide a voltage source.

 The circuit according to the invention is based on the idea that
15 freely oscillating inverter circuits are used for balancing the voltages of series-connected capacitors, the inverter circuits converting voltage supply into voltage to be used in other circuits during voltage balancing. The circuit according to the invention provides the advantage that the power that would otherwise be lost can be utilized in low-power circuits, e.g. as auxiliary voltage in the control circuits and gate drivers of a frequency converter.
20

BRIEF DESCRIPTION OF THE DRAWINGS

 The invention will be described in greater detail by means of preferred embodiments with reference to the accompanying drawings, in which

 Figure 1 illustrates a balancing circuit for voltages of a series connection of capacitors according to the invention; and
25

 Figures 2 and 3 illustrate inverters used for balancing voltages of capacitors according to the invention.

DETAILED DESCRIPTION OF THE INVENTION

 Figure 1 illustrates a balancing circuit for voltages of a series connection of capacitors according to the invention. The capacitors of Figure 1 are
30 illustrated as capacitors of the intermediate circuit of a frequency converter but the circuit according to the invention can also be utilized in any other applications of the series connection of capacitors. The intermediate circuit of the frequency converter shown in the figure comprises a positive 1 and a negative 2

voltage busbar, between which there are three capacitors 3 connected in series. Figure 1 does not illustrate the actual inverter part of the frequency converter, i.e. power semiconductors and their control circuits because these components are irrelevant to the application and understanding of the invention.

According to the invention, a freely oscillating inverter 4 is connected to the poles of each series-connected capacitor. Examples of freely oscillating inverters are shown in Figures 2 and 3. According to Figure 1, the freely oscillating inverters are connected so that each series-connected capacitor comprises an inverter of its own. Thus the positive pole of each inverter is connected to the positive pole of the respective capacitor and the negative pole to the negative pole of the respective capacitor.

Using the circuit according to the invention the voltage of the electrolyte capacitors functioning as the power reserve of an intermediate circuit in a frequency converter can be balanced so that the auxiliary power needed by the frequency converter is generated at the same time. A freely oscillating inverter formed by two transistors 11, 12 and a converter 13 is connected in parallel with each capacitor, the centre 15 of the primary coil 14 of the converter being connected to the positive pole of the capacitor and the free ends of the same coil to the collectors of the transistors. This kind of embodiment of the invention is shown in Figure 2. Still referring to Figure 2, the emitters of the transistors 11, 12 are connected to the negative pole of the capacitor and the bases as well as a few passive components to the control coil of the converter according to the prior art to form a self-oscillating inverter. The self-oscillating inverter generally refers to the fact that no separate control circuits or timing circuits are used for controlling the semiconductor switches of inverter of this kind. Using passive components it is possible to provide an inverter in which there is no need for separate control power because the semiconductors conduct alternately thanks to oscillation.

In the self-oscillating inverter alternating voltage is induced in the secondary coil 16; 26 of the converter 13; 23, which is rectified with a rectifying bridge 17; 27 to provide a direct current that can be used as the auxiliary voltage. The full-wave rectified outputs of all inverters are connected together as shown in Figure 1 to provide an intermediate voltage V_a , e.g. $24\text{ V} \pm 30\%$, which is suitable for supply of auxiliary power. The number of turns of the secondary coil of the converters in the inverters is adjusted with respect to the

primary coil so that this voltage is achieved with a typical terminal voltage of the energy reserve capacitor. It is clear that large tolerances should be allowed for this intermediate auxiliary voltage because the range of variation of the primary voltage is also large.

5 Depending on the inverter solution used, the transistors of the inverter should withstand a voltage which is 1.2 or 2 times the capacitor voltage, which in the worst case, i.e. with a mains voltage of 500 volts and an overvoltage of 30%, is 878 volts. Thus it is possible to select transistors intended for a collector voltage of 1000 volts, in which case the inverter circuit is at its simplest (Figure 2), or transistors of 600 volts, in which case a few additional
10 components are needed for the circuit (Figure 3).

 The output and input voltages of the inverter circuit shown follow each other when multiplied by the transformation ratio of the converter, i.e. if the input voltage increases, rectified output voltage also increases accordingly.
15 The influence of any unideal properties of the inverter on this dependency is typically very small.

 Since the rectified outputs of the converters are connected together as shown above so that they retain their polarity, the auxiliary voltage power flows mainly through the converter with the higher secondary voltage. This
20 means that most of the auxiliary voltage power is taken from the energy reserve capacitor the terminal voltage of which tends to be the highest with respect to the other series-connected capacitors. Thus energy flows out of the capacitor, which means that the terminal voltage of the capacitor decreases until it reaches the second highest terminal voltage. However, this interaction
25 occurs simultaneously between all capacitors, and consequently the terminal voltages will be equal and the supply of auxiliary power is divided almost evenly between the inverters.

 This solution provides active balancing of energy reserve capacitors with virtually no loss of power, while the circuit can be used for supplying auxiliary voltage, e.g. in connection with a frequency converter.
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 An additional advantage of the invention is that the components of the inverters are not dependent on the mains voltage. In all cases the number of series-connected inverters comprising the same components increases along with the input voltage, and thus expensive high-voltage switch components are not needed even with high supply voltages, which is the case in solutions based on separate auxiliary current sources driven directly by the in-
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intermediate circuit voltage. If the supply voltage is around 1000 volts, standard switch components are not even available.

5 The output power of frequency converters tends to be higher when the supply voltage is high. In that case one needs more auxiliary power because the number of gate drivers is larger, but thanks to several series-connected intermediate circuit capacitors, a corresponding number of inverters the secondary circuits of which are connected in parallel are available for this purpose.

10 The inverter and its secondary rectifier used in the circuit according to the invention can be implemented easily using a piece of a circuit board, or as a unit cast in epoxy, which, being a low-loss unit, is easy to connect directly to the poles of a power capacitor. The price of such a volume component can be reduced considerably, and, if necessary, the converters can be provided with reinforced insulation, in which case the intermediate voltage of the auxil-
15 iary power can be rendered to the earth potential to enable parallel battery use when the intermediate circuit voltage has not been switched on or it is too low with respect to the normal operation. It is also easy to implement a blocking circuit for accidental start-up in the earth potential.

20 It is obvious to a person skilled in the art that the inventive concept can be implemented in various ways. The invention and its embodiments are thus not limited to the examples described above, but they may vary within the scope of the claims.

CLAIM

A balancing circuit for voltages of a series connection of capacitors, particularly for intermediate circuit capacitors (3) of an inverter, there being at least two intermediate circuit capacitors connected in series over intermediate circuit voltage, **characterized** in that the balancing circuit comprises capacitor-specific freely oscillating inverters (4), the input poles of which are connected in parallel with the capacitor corresponding to the inverter and the output poles of which are connected in parallel to provide a voltage source (Va).

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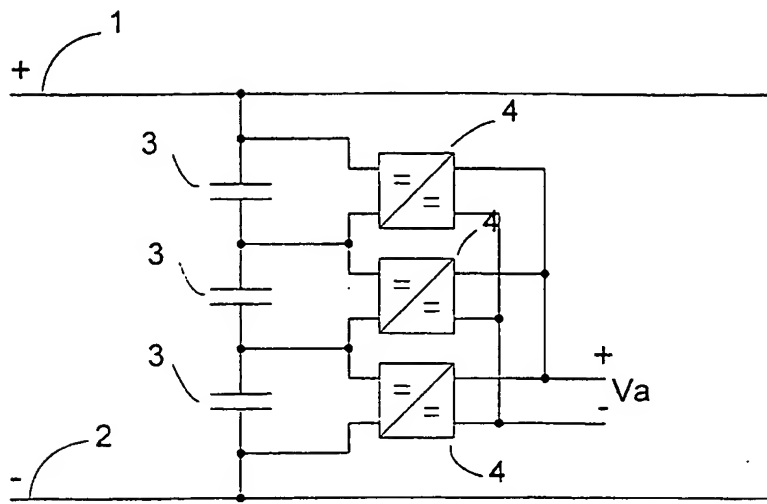


FIG. 1

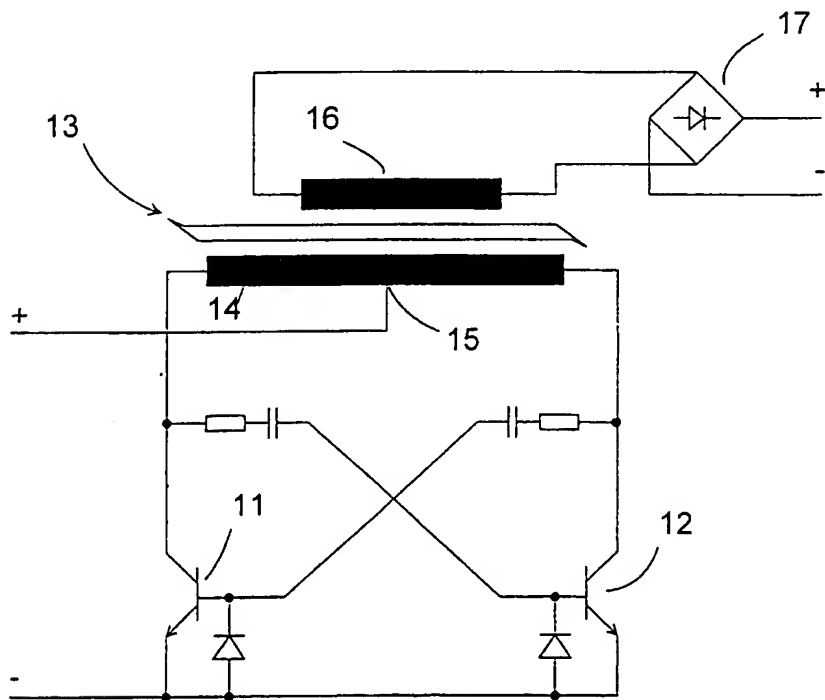


FIG. 2

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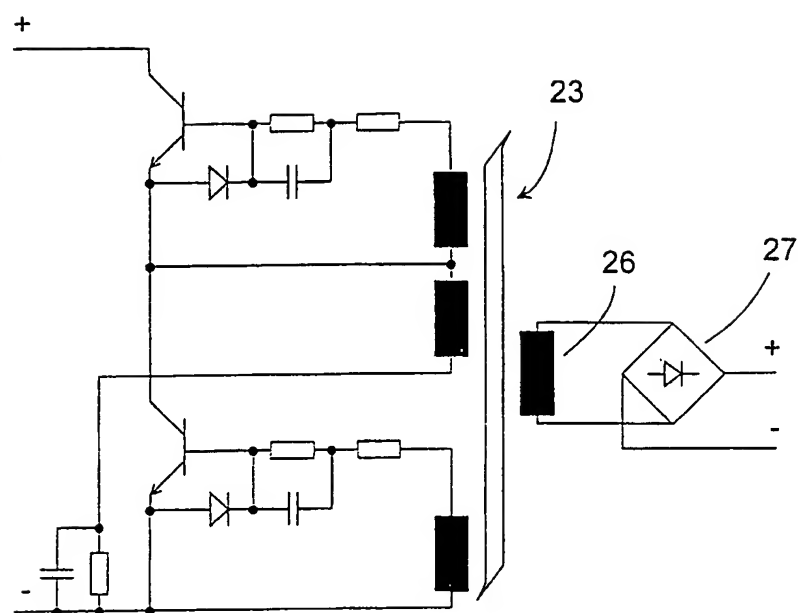


FIG. 3

INTERNATIONAL SEARCH REPORT

International application No.

PCT/FI 00/00806

A. CLASSIFICATION OF SUBJECT MATTER

IPC7: H02J 7/00, H02M 5/40

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC7: H02M, H02J, H02H

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	EP 0911950 A2 (GENERAL ELECTRIC COMPANY), 28 April 1999 (28.04.99), page 3, line 57 - page 4, line 4 --	1
Y	WO 9716879 A1 (XICON AB), 9 May 1997 (09.05.97), page 2, line 7 - line 13; page 3, line 1 - line 20 --	1
Y	DE 19708842 A1 (KAHLEN, HANS), 10 Sept 1998 (10.09.98), column 1, line 1 - line 41 -- -----	1

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Information on patent family members

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Patent document cited in search report			Publication date	Patent family member(s)		Publication date
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